

## TRANSMITTER POWER AMPLIFIER RAMPING METHOD

## ABSTRACT OF THE DISCLOSURE

5 A method for reducing frequency glitches in a  
digital transceiver due to power amplifier input  
impedance variations. According to the method, the power  
amplifier is switched on after the end of a prior packet  
reception period and before a new packet transmission  
begins. Instead of ramping the power amplifier gain, the  
10 method ramps the modulation signals. As a consequence,  
any VCO frequency transients that may result from turning  
on the power amplifier have an opportunity to decay  
before the new packet transmission is initiated. This  
technique effectively isolates the transmitter power  
15 amplifier from the frequency synthesizer VCO to  
facilitate fast switching transceiver operation.

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